

This module gives graduate students in the UCD School of Electrical & Electronic Engineering a grounding in the principles and practice of university teaching and laboratory demonstration. The module is specifically for graduate students who are actively engaged in the role of Teaching Assistant within the School of Electrical, Electronic and Communications Engineering.

Participants in the module will be introduced to the practicalities of laboratory demonstrating, including preparation for relevant laboratory work and laboratory safety. In addition, they will be given an introduction to relevant works in the teaching and learning literature (both generic and discipline-specific), and they will learn how to apply these ideas in their own teaching. They will learn to assess/critique their teaching based on feedback gained from their peers and/or their students. They will be encouraged to take active responsibility for judging and indeed, helping to maintain, the quality of their module's laboratory assignments.

Module participants must be registered/about to register for a Masters/PhD degree within the School of Electrical & Electronic and Engineering and must be in an active teaching assistant role.

This module is graded on a Pass/Fail basis.

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<div style="text-align:center;"><p>Curricular information is subject to change</p></div>

What will I learn?

Learning Outcomes:

<p>On completion of this module, a participant will be able to:

1. Demonstrate a firm understanding of the technical content of the laboratory, and an ability to anticipate as well as to react to students' problems in the lab;
 2. Demonstrate a familiarity with some of the key relevant literature regarding teaching and learning, both generic and specifically within the field of Electrical & Electronic and Engineering;
 3. Take responsibility for evaluating their own performance based on feedback from a variety of sources (student, peer, laboratory supervisor) and demonstrate a willingness to change their teaching practice in light of this feedback;
 4. Understand the relationship between the design of a laboratory session and the learning outcomes associated with it;
 5. Demonstrate an awareness of UCD, College and School policies and procedures that impact on their role as a Teaching Assistant.
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How will I learn?

Student Effort Hours:

Student Effort Type	Hours
Small Group	10
Specified Learning Activities	50
Autonomous Student Learning	40
Total	100

Am I eligible to take this module?

<div class="subHeadCB">Requirements, Exclusions and Recommendations</div>

Learning Requirements:

<p>Participants must be graduate-level students who are in an active teaching role within the UCD School of Electrical, Electronic and Communications Engineering.

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How will I be assessed?

Assessment Strategy

Description	Timing	Open Book Exam	Component Scale	Must Pass Component	% of Final Grade
Continuous Assessment: Various assignments	Varies over the Trimester	n/a	Graded	No	100

<div class="row">
<div class="col-sm-6">Carry forward of passed components
No</div>
</div>

What happens if I fail?

Resit In	Terminal Exam
Spring	No

Assessment feedback

<div class="subHeadCB">Feedback Strategy/Strategies</div>
<p>* Feedback individually to students, post-assessment
* Group/class feedback, post-assessment
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<div class="subHeadCB">How will my Feedback be Delivered?</div>
<p>Not yet recorded.</p>

Reading List

<div class="pageBreak"><nav class="white-box no-left-arrow zero-top-margin">
<h1 class="printOnly"> UCD Course Search
Teaching Assistant in EECE (EEN40530) </h1><h3 class="printOnly">Academic Year 2019/2020</h3><p class="printOnly">The information
contained in this document is, to the best of our knowledge, true and accurate at the time of publication, and is solely for informational purposes.
University College Dublin accepts no liability for any loss or damage howsoever arising as a result of use or reliance on this information.</p>
<h4 class="noPrint">Teaching Assistant in EECE (EEN40530)</h4>
<dl>
<dt>Subject:</dt>
<dd>Electronic & Electrical Eng</dd>
<dt>College:</dt>
<dd>Engineering & Architecture</dd>
<dt>School:</dt>
<dd>Electrical & Electronic Eng</dd>
<dt>Level:</dt>
<dd>4 (Masters)</dd>
<dt>Credits:</dt>
<dd>5.0</dd>

<dt>Trimester:</dt>
<dd>Autumn</dd>
<dt>Module Coordinator:</dt>
<dd>Dr Paul Cuffe</dd>
<dt>Mode of Delivery:</dt>

<dd>Face-to-Face</dd>
<dt>Internship Module:</dt><dd>No</dd>

<dt>How will I be graded?</dt>
<dd>Pass/Fail (GPA Neutral) </dd>

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<div class="noPrint" style="text-align:center; margin-top:10px;"><button class="menubutton" onclick="window.print()"><i class="fa fa-print fa-fw"> Print
Page</button>
(Google Chrome is recommended when printing
this page)</div>

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